

## **Behavioral group meeting held on November 1, 2002**

### **Present**

Richard Shi, Bo Hu, Bo Wan, Lili Zhou, Erik Normark, Renpi Yu, Yong Wang, Chris Baker, Kishore Tanikella, Gus Mak, Pavel Nikitin

### **Highlights**

- New team member: Erik Normark

### **Progress/current work**

- EKV modeling (Gus, Chris, Lili)
- Understanding behavioral modeling of D/A converters (Kishore)
- Boeing designs (Yong)
- Compiler improvement (Bo Hu, Bo Wan)

### **Future work plan**

- Fix the problems with posted compiling instructions
- Run posted test examples using posted instructions
- Continue with EKV model and run it in Verilog-AMS (Chris, Gus)
- Assess current state-of-the-art in A/D converter modeling (Kishore)
- Digital VHDL simulation (Erik, Kishore)
- Simulate Kishore's M.S. thesis A/D design project with Hspice and with BMAC using Lili's BSIM3 model to test both the model and the compiler (Kishore, Lili)
- Run examples on Hamster AMS and Dolphin Smash simulators
- Preparation of a conference paper on BMAC (Bo Wan, Bo Hu, Lili)
- Think about giving presentation to the group once you have accumulated enough technical results and/or problems