DARPA NEOCAD UW PROGRAM QUARTERLY REVIEW MEETING AGENDA

June 19-20, 2003 ♦ Seattle, WA

(**UW EE/CSE Building Room 403** For **map and driving directions**, please visit
http://www.ee.washington.edu/contact.html)

June 19, Thursday, 2003: UW Circuit Modeling, Simulation, Synthesis Review

8:30 – 9:00	Welcome
	EE Associate Chair Mari Ostendorf and Prof. Richard Shi
9:00 – 9:30	CoSMoS Project Overview
	Dr. Pavel Nikitin, Prof. Vikram Jandhyala
9:30 – 10:00	Parasitic-Aware Analog/RF Circuit Synthesis
	Prof. David Allstot, University of Washington
10:00 – 10:30	Pulse-based Clockless Digital Logic Synthesis
	Prof. Carl Sechen, University of Washington
10:30 – 10:45	Morning Break
10:45 – 11:15	CoSMoS Validation Report from Boeing
	Tom Friddell, Boeing Electronics.
11:15 – 11:45	DARPA NeoCAD Technology Transfer Report from Orora
	Ellis Smith, President & CEO, Orora Design Technologies
11:45 -- 1:00	Working Lunch Break
1:00 – 1:30	Progress on Behavioral/Device Model Compiler & Model in a Day Flow
	Bo Wan, Bo Hu, Lili Zhou, Univ. Washington
1:30 – 2:00	Progress on Linear, Nonlinear and Time-Domain Solvers (SRC)
	Dr. Guoyong Shi, Zhou Li, Univ. Washington
2:00 – 2:30	Progress on VHDL-AMS Extension for EM and Mixed-Technology Modeling
	Dr. Pavel Nikitin, University of Washington
2:30 – 3:00	Progress on Parasitic-Aware Digital/Analog Behavioral Modeling
	Sam Bhattacharya and Lei Yang, Univ. of Washington
3:00 -- 3:30:	Break, Refreshment & Reception (Student Posters)
3:30 – 4:00:	Progress on Parasitic-Aware Layout Synthesis (IPRAIL)* (NSF)
	Nuttorn Jangkrajarng, University of Washington
4:00 – 4:45pm:	Progress on Livermore EIGER EM Modeling, Dr. Rob Sharpe, LLNL
4:45 – 5:30pm:	Progress on Livermore EMSolve Modeling, Dr. Dan White, LLNL
5:30 – 6:00pm	Closed Feedback from Government Team
7:00 – 9:00pm:	Dinner Cruise // Student Poster

June 20, 2003, Friday Morning (UW/LLNL EM Review)

8:30–9:00 am	UW EM Modeling Summary, Prof. Vikram Jandhyala
9:00 –9:20	Time Domain Microelectronic Simulation for EM and EM-circuit effects
	Chuanyi Yang (NSF)
9:20 –9:40	Exact Formulations for Stable Time Domain Simulation for Realistic Meshes and Structures

	Gong Ouyang
9:40 –10:00	Superfast Parasitic Extraction for Frequency Domain EM-Circuit Simulation
	Dipanjan Gope (SRC-NSF)
10:00-10:20	Realistic Material Effect Simulation in the Frequency Domain
	Swagato Chakraborty
10:20-10:40	Frequency Domain Codebase, Coupling, and Fast Solution
	Todd West
10:40 – 11:10	Morning Break and EM Posters/Demos
11:10 – 12:00:	Government Feedback and End of Review